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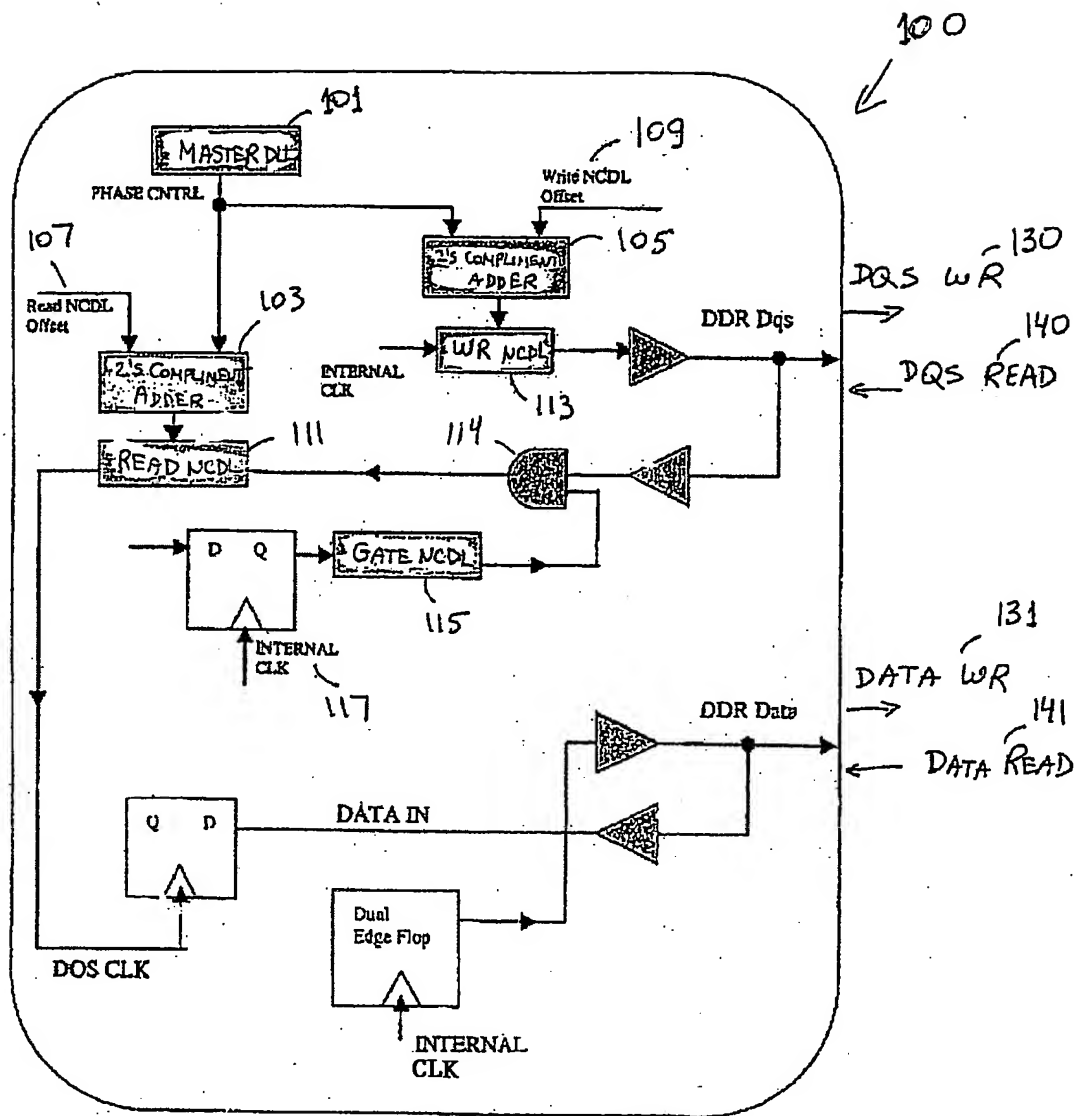


FIG. 1A. DLL and NCDL mechanism in DDR memory controller

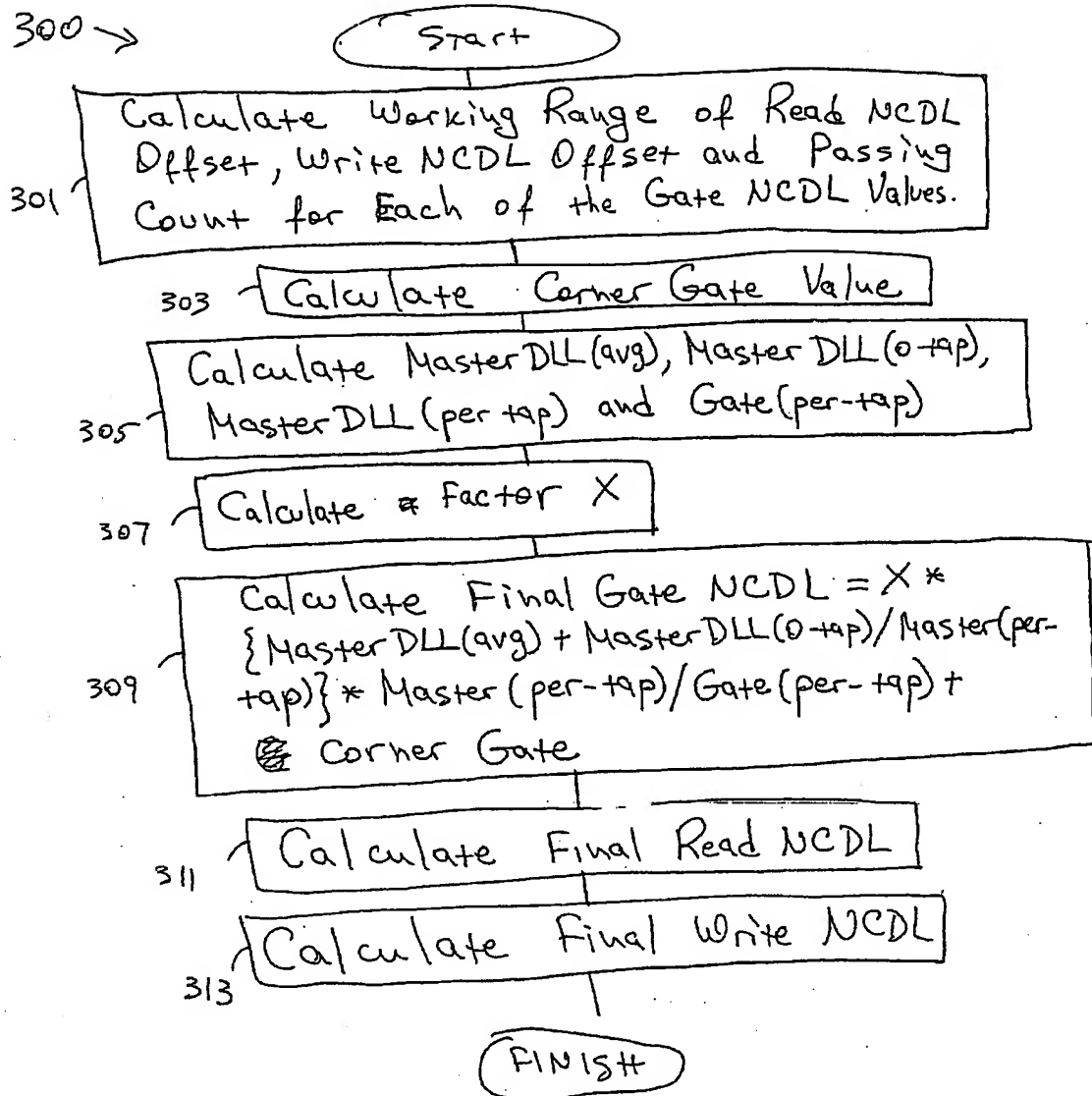


FIG. 3